IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**AMENDMENT** UNDER 37 C.F.R. 1.116

24/21/2006

2123

Examiner:

J. Proctor

Applicant(s): J. G. Walacavage et al.

Serial No.:

09/965,904

Filing Date:

September 28, 2001

For:

METHOD OF LOGICAL MODELING OF

**OPERATOR INTERACTION WITH** 

PROGRAMMABLE LOGIC CONTROLLER

LOGICAL VERIFICATION SYSTEM

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Dear Sir:

This is in response to the Office Action dated January 31, 2006, regarding the above-identified application.